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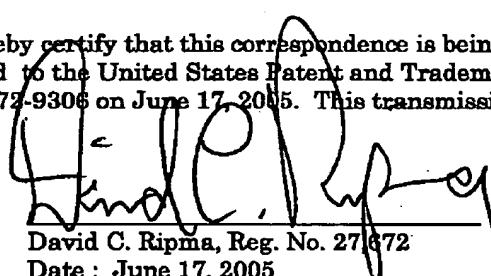
JUN 17 2005

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of)	<u>PATENT APPLICATION</u>
)	
Inventor: Yoshi Ono, John F. Conley, Jr.,)	June 17, 2005
and Pooran Chandra Joshi)	
)	<u>ATTORNEY DOCKET</u>
Serial No.: 10/805,158)	No. SLA0830
)	
Filed: March 19, 2004)	Group Art Unit: 2814
)	
Title: CHARGE TRAP NON-)	Examiner: Marcos D.
VOLATILE MEMORY)	Pizarro-Crespo
STRUCTURE FOR 2 BITS)	
PER TRANSISTOR)	Confirmation No. 8642
)	

CERTIFICATE OF TRANSMITTAL UNDER 37 C.F.R. § 1.8

I hereby certify that this correspondence is being facsimile transmitted to the United States Patent and Trademark Office at Fax. No. (703) 872-9306 on June 17, 2005. This transmission contains a total of 2 pages.



David C. Ripma, Reg. No. 27872
Date : June 17, 2005

RESPONSE TO RESTRICTION REQUIREMENT

Mail Stop Non-Fee Amendment
Hon. Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Sir:

This communication responds to an Office Action dated

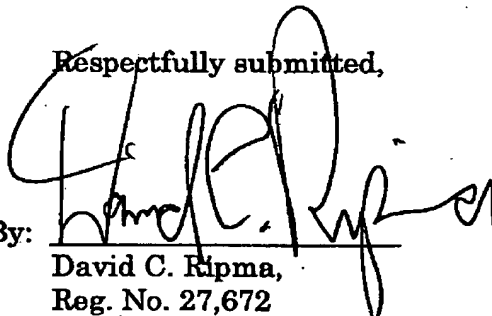
May 18, 2005 in the above-identified patent application. In the Office
Action the Examiner requires restriction to one of the following
inventions:

Group I. Claims 1-15, drawn to a device; and

Group II. Claims 16-26, drawn to a method.

Applicants elect to pursue the Group II claims, claims 16-26,
drawn to a method.

Date: 6/17/05

Respectfully submitted,

By: David C. Ripma,
Reg. No. 27,672

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